

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Pierre ROGIER

Serial No.: To Be Assigned

Filed: April 7, 2000

For: PROCESS FOR IMPROVING THE  
PERFORMANCE OF A MULTIPROCESSOR  
SYSTEM COMPRISING A JOB QUEUE AND  
SYSTEM ARCHITECTURE FOR  
IMPLEMENTING THE PROCESS

Examiner:

Group Art Unit:

Corres. To FR99/04337

Filed April 7, 1999

McLean, Virginia

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

Sir:

In compliance with the duty of disclosure set forth in 37 CFR 1.56, it is requested that the documents listed on the attached Information Disclosure Statement By Applicant (PTO/SB/08A) be made of record in this application. These documents were cited in the corresponding French application No. 99 004337 filed on April 7, 1999. A copy of the "Rapport De Recherche Preliminaire" (Preliminary Search Report) is enclosed. In compliance with the requirements of 37 CFR 1.97 and 1.98, the possible relevance of the documents is noted on the attached Information Disclosure Statement by Applicant (PTO/SB/08A), and copies of the documents are attached.

Without any assertion of prior art effect, the documents on the attached Form PTO/SB/081 are called to the Examiner's attention. Although



the relevance of each document has been briefly indicated, the Examiner is urged to make his own determination of relevance and no representation is made nor intended that "better" art is not available.

Respectfully submitted,

Miles & Stockbridge P.C.

Date: April 7, 2000

By: 

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